



#11/B
500.39910X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. MIYAZAKI et al.

Application No.: 09/811,387

Filed: March 20, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
IN WHICH OPERATING FREQUENCY, SUPPLY
VOLTAGE AND SUBSTRATE BIAS VOLTAGE ARE
CONTROLLABLE TO REDUCE POWER CONSUMPTION

Art Unit: 2816

Examiner: Kenneth B. Wells

7/15/03
Adm Hs

**SUBMISSION OF AMENDMENT IN
ACCORDANCE WITH 37 CFR § 1.114**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 11, 2003

Sir:

In accordance with the provisions of 37 CFR § 1.114, entry of the following
amendments is respectfully requested:

IN THE CLAIMS:

Please **cancel** claims 31-35 without prejudice or disclaimer of the subject
matter contained therein.

Please **amend** claims 17, 20, 21, 25, 26 and 27 as follows:

17. (Amended) A semiconductor integrated circuit device comprising:

a first circuit including at least one MOS transistor;

a second circuit to control a frequency of a clock signal to be supplied to the
first circuit;

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